

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	) Examiner: Unknown
Iris Plaxton et al.	)
	) Art Unit: 2152
Application No.: 09/800,935	)
	) Docket No.: SC11641TS
Filed: March 7, 2001	) Date: May 2, 2001
Title: METHOD AND DEVICE FOR	)
CREATING AND USING PRE-	)
INTERNALIZED PROGRAM FILES	
Commissioner for Patents	
Washington, DC 20231	

### PRELIMINARY AMENDMENT UNDER 37 C.F.R. § 1.115

Dear Assistant Commissioner:

Applicants respectfully request entrance of the following amendments in the current Application.

#### **FEES**

Applicants authorize charging Deposit Account No. 13-4773 in the amount of \$36.00 in connection with this response, and Applicants continue to authorize the payment of any other fees owed in connection with this application, or credit Deposit Account No. 13-4773 for any refunds. One copy of this page is enclosed for deposit account purposes.

05/10/2001 DTESSEM1 00000100 134773 09800935

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Applicants respectfully request the following amendments and submit the following remarks.

Please amend the specification as follows:

### IN THE CLAIMS:

Please amend the claims as indicated in both clean form and separate detailed form.

- 35. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external to the device and wherein one of the first memory or the second memory comprises a fourth set of one or more instructions, the fourth set of one or more instructions updating a memory address within the reusable executable image when executed by the processor.
- 36. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external of the device and wherein one of the first memory or the second memory comprises:
  - a fourth set of one or more instructions, the fourth set of one or more instructions when executed by the processor moving the reusable executable image to a different location.
- 46. (Amended) The device of claim 44, wherein the virtual machine further comprises:
  - a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor transferring the pre-internalized image of the selected program file from a second memory.



- 51. (New Claim) The device of claim 36 further comprising:
  - a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor updating memory address within the reusable executable image.
- 52. (New Claim) The device of claim 46 further comprising:

  a sixth set of one or more instructions, the sixth set of one or more instructions when executed by the processor updating memory addresses within the pre-internalized image of the selected program file, wherein execution of the fifth set of one or more instructions and the sixth set of one or more instructions

#### REMARKS

corresponds to the pre-internalization mode of operation.

Applicants respectfully request the amendment of claims 35, 36 and 46 and the addition of two new dependent claims 51 and 52. Applicants now request consideration of the pending claims 1-52. The additional fees of \$36.00 are a result of the addition of two claims in excess of twenty, and the fees are authorized to be charged to Applicants' Deposit Account No. 13-4773. No amendment made was related to the statutory requirements of patentability unless expressly stated herein. No amendment made was for the purpose of narrowing the scope of any claim, unless Applicant has argued herein that such amendment was made to distinguish over a particular reference or combination of references. Please contact

me if there are any issues regarding this communication or the current Application.

Respectfully submitted,

Robert L. King

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### Another Version of the Claims Under 37 C.F.R. 121(c)

- 35. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external to the device and wherein one of the first memory or the second memory comprises a fourth [plurality of ] set of one or more instructions, the fourth set of one or more instructions updating a memory address within the reusable executable image when executed by the processor.
- 36. (Amended) The device of claim 27 further comprising a second memory either contained within the device or external to the device and wherein one of the first memory or the second memory comprises:
  - a fourth set of one or more instructions, the fourth set of one or more instructions when executed by the processor moving the reusable executable image to a different location [within the second memory or transferring the pre-internalized image of the selected program file from a second memory; and
  - a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor updating memory address within the reusable executable image].
- 46. (Amended) The device of claim 44, wherein the virtual machine further comprises:
  - a fifth set of one or more instructions, the fifth set of one or more instructions when executed by the processor transferring the pre-internalized image of the selected program file from a second memory[; and

a sixth set of one or more instructions, the sixth set of one or more instructions when executed by the processor updating memory addresses within the pre-internalized image of the selected program file, wherein execution of the fifth set of one or more instructions and the sixth set of one or more instructions corresponds to the pre-internalization mode of operation].

- 51. (New Claim) The device of claim 36 further comprising:

  a fifth set of one or more instructions, the fifth set of one or more instructions

  when executed by the processor updating memory address within the reusable executable image.
- 52. (New Claim) The device of claim 46 further comprising:

  a sixth set of one or more instructions, the sixth set of one or more

  instructions when executed by the processor updating memory

  addresses within the pre-internalized image of the selected

  program file, wherein execution of the fifth set of one or more
  instructions and the sixth set of one or more instructions
  corresponds to the pre-internalization mode of operation.



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